

## Rajya Vokkaligara Sangha

Rajya Vokkaligara Sangha was found in the year 1906 to promote social, cultural and educational aspirations of the agricultural community. Rajya Vokkaligara Sangha with a commanding presence in Bengaluru, Ramanagara, Mandya, Mysore, Chikkamagalur, Hassan, Tumkur, Chikkaballapura, Kolar and Chitradurga districts and with a fairly well spread presence in other regions of Karnataka has been conducting number of regular activities for the development and well-being of the community in particular and to all sections of the society in general. The Sangha has also provided hostel facilities for poor background students who are making use of this hostel facilities during their course of education.

## Bangalore Institute of Technology (BIT)

BIT Affiliated to Visvesvaraya Technological University, Belagavi, is a Premier Engineering College in Bengaluru, Karnataka. The College was established in August 1979 by Rajya Vokkaligara Sangha. BIT is recognized by AICTE, New Delhi and all of its UG programmes and MCA are accredited by NBA. The Institute offers 10 UG Programmes, 10 PG programmes and 13 VTU recognized Research Centers offering Ph.D. Programmes.

## Department of Electronics and Telecommunication Engineering (ETE)

The Department of ETE was started in the year 1992-93. The Department has a team of well qualified and dedicated Staff members. The Department is accredited by NBA.

## About Workshop

The workshop is designed to meet the requirements of the newly introduced VTU Wireless Communication laboratory Syllabus. It also aims at providing participants with a focused view on Wireless

Communications and Networks in the context of the recent technological advances in the field. The workshop will comprise of lectures and hands-on sessions by experts from both academia and industry who will be introducing the various aspects of recent advances in Wireless Communication.

### Chief Patrons

**Sri. Karee Gowda, IAS**

*Administrator, RVS*

**Sri. C.Siddaramiah, KGS(R)**

*CEO, RVS*

### Patron

**Dr. Aswath M.U.**

*Principal, BIT*

### Convener

**Dr. S.Shanthala**

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### Coordinators:

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### Who can attend?

The workshop is open to Engineering faculty, PG Students, Research Scholars, practicing Wireless Communication Engineers, and Industry personnel.

Online Platform:



Register by 25<sup>th</sup> Sep 2021

Free Registration

Registration Link: <https://forms.gle/YTYgrcsaMasZ7LS29>



## BANGALORE INSTITUTE OF TECHNOLOGY

(Affiliated to VTU, Belagavi, Approved by AICTE, Accredited by NBA (9UG Programmes & MCA))

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Department of Electronics and Telecommunication  
Engineering

Presents

**3 Day National Level Workshop**

on

**“Wireless Systems Design Using Simulink  
and Vivado”**

28<sup>th</sup> September – 30<sup>th</sup> September 2021

in Association with



Enabling Excellence



## About CoreEL Technologies

CoreEL Technologies is a Customer Application Specific Product & Solutions (CASPS) Company offering INNOVATIVE solutions from its diverse portfolio of expertise that includes Intellectual Property (IP) cores, System Design, Manufacturing, Sustenance and OEM solutions in the form of EDA tools, CAE tools, COTS Products and Technology training. CoreEL's Strength lies in its ability to blend deep domain knowledge with the right ingredients across its portfolio of offerings. It is a leading developer of Advanced Electronic System Level Products and Solutions to three primary markets – Aerospace & Defence, Digital Media Broadcast, and Universities & Institutions of higher learning.

## SCHEDULE

Date	Time	Speaker	Topic
<b>DAY 1</b> 28-09-2021 Tuesday	10:00 AM to 10:30AM		Inaugural Function
	10:30 AM to 12:00 PM	Dr. S. Ravi Shankar <i>Professor, RVCE</i> <i>Ex Consultant, Centillium Communications &amp;</i> <i>Ex Senior Manager, Texas Instruments</i>	Key note Address
	02:00 PM to 04:00 PM	Team CoreEL Technologies	<b>Introduction to Communication System Modelling using Simulink</b> <ul style="list-style-type: none"> <li>• Model Based Design using Simulink</li> <li>• Introduction to Communication System Toolbox</li> <li>• Bit Error Rate Analysis</li> <li>• Channel Modelling</li> <li>• Apps for Quick Analysis of Communication Systems</li> </ul>
<b>DAY 2</b> 29-09-2021 Wednesday	10:00 AM to 12:00 PM	Team CoreEL Technologies	<b>Prototyping and Implementation Workflow for FPGA/ASIC Design</b> <ul style="list-style-type: none"> <li>• Challenges with Traditional Implementation workflow</li> <li>• Generate VHDL and Verilog Code from Simulink.</li> </ul> <b>Introduction to Hardware Development using Vivado</b> <ul style="list-style-type: none"> <li>• Introduction to Vivado Design Flow</li> </ul>
	02:00 PM to 04:00 PM		<b>System Design flow using Xilinx Vivado and Vitis</b> <ul style="list-style-type: none"> <li>• Overview of FPGA Architecture</li> <li>• Hardware Design Flow using Vivado IP Integrator</li> <li>• Software Design Flow using Vitis Applications</li> </ul>
<b>DAY 3</b> 30-09-2021 Thursday	10:00 AM to 11:30 AM	Dr. Navin Kumar <i>Associate Professor and Chairman,</i> <i>ECE Department, Amritha Vishwa</i> <i>Vidyapeetham (University), Bangalore Campus.</i> <i>IEEE Secretary - Bangalore Section &amp;</i> <i>Senior Member of IEEE</i>	Challenges and Opportunities in 5G and 6G
	11:30 AM		Valedictory